



GOVERNMENT COLLEGE OF ENGINEERING, KARAD

(An autonomous institute of Govt. of Maharashtra)

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GCEK/EED/Quotation-eZdsp/2017/

Date – November 17, 2017

Department of Electrical Engineering Invitation for Quotation for supply of material

To,

Subject: invitation for quotation for supply of eZdsp starter kits....

Sealed quotations are invited from the authorized dealers / suppliers for supply of eZdsp laboratory trainer starter kits with following details:

1. eZdsp laboratory trainer starter (basic trainer starter) kits:
TMS320F2812 with Power Supply and Code Composer Studio

Other Technical Details of the kit:

- TMS320F2812 Digital Signal Processor operating at 150 Mhz.
- 18 K on chip RAM; 128 K on chip FLASH ROM; 64 K words on board RAM
- Expansion Connectors; Onboard embedded IEEE 1149.1 JTAG controller
- +5 volt operation supplied with adapter
- Onboard IEEE 1149.1 JTAG emulation connector
- Compatible with eZdsp Wire Wrap Prototype Card
- RoHS Compliant

Software Details:

- Code Composer Studio for the above kit - Compatible with C2000 Code Composer from Texas Instruments (Includes 'C' compiler, assembler, linker, debugger)
- Compatible with SDFlash programming utility from Spectrum Digital
- Third-party development tools (VisSim/TI C2000 Rapid Prototyper from Visual Solutions and VisSim/Embedded Controls Developer from Visual Solution)

Other Terms and conditions:

1. The quotation shall include the unit price of the item including applicable taxes (or extra as applicable), GST number of the supplier
2. Quotation shall be valid for at least 01 months.
A demo shall be arranged during supply of the kits and installation of software.

The Quotations shall be addressed and reached on or before November 24, 2017 at

To

The Principal / The Head, Department of Electrical Engineering

Government College of Engineering

Vidyanagar, KARAD

Dist. Satara (Maharashtra state)

PIN – 415124


(Head, Department of Electrical Engineering)