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## **Government College of Engineering Karad**

(An Autonomous Institute of Government of Maharashtra) Vidyanagar, Karad, Maharashtra 415124, India

## Tabulation Register(R08)

## SCHEME: ELECTRONICS & TELE-COMMUNICATION ENGINEERING-2015-16 [7]

## FIFTH SEMESTER ELECTRONICS AND TELE-COMMUNICATION ENGINEERING, B.TECH., EXAMINATION HELD IN SUMMER TERM-2018

		Subject Sl.No.		1		2	3		4		5	6		7		8		9		10		11	12		Present		Previous	Total	Present	
Sr. No	Student Regno	Subject Code	EX	501	EX!	502	EX5	03	EX5	04	EX505	EX	(506	EX5	507	EX	508	EX50	9	HS003	3				Credit	SGPA	Credit	Credit	CGPA	Student Regno
		Credit(C)		4	3	3	4		3		4		1	1	1		1	1		3					Present		Previous	Total		
		Name	grd	C.G	grd	C.G	grd	C.G	grd	C.G	grd C.G	grd	C.G	grd	C.G	grd	C.G	grd	C.G	grd C.	G	grd C.G	grd	C.G	EGP		EGP	EGP		
		Nilkanth Abhishek									_		-				$\neg$													

Total Student Count:

EX501- Digital Communication EX504- Control Systems EX507- Embedded Systems Lab HS003- General Proficiency-III

EX502- Embedded Systems EX505- Mini Project using Design Tools EX508- Digital Signal Processing Lab

EX503- Digital Signal Processing EX506- Digital Communication Lab EX509- Instrumentation and Control Lab -

Contoller of Examination

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Note: ' \* ' Denotes backlog subjects

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