

## Resume

### POOJA TANURKAR

Mob: +91 9960418739

Email: poojastanur@gmail.com

LinkedIn: [www.linkedin.com/in/pooja-tanurkar-24747a76](http://www.linkedin.com/in/pooja-tanurkar-24747a76)

KRISHANAKUNJ, TEACHER COLONY,  
DHARMABAD, NANDED-431809

### Objective:

To join and grow with an esteemed organization, where I can effectively utilize my knowledge and skills.

### Profile at a Glance:

- ⇒ Have Completed **BE** in **Electronics and Telecommunication**.
- ⇒ Strong troubleshooting and problem-solving skills
- ⇒ Ability to adapt well and perform in new environment.
- ⇒ Have good Knowledge in cross technology, Satellite communication.
- ⇒ Can work well in both team environment and individual assignments.
- ⇒ A motivated and diligent team player utilizing excellent communication, interpersonal skills with strong analytical mind-set to learn quickly, to analyze the situation, identify requirements and provide solutions.

## EXPERIENCE

Designation	College/University	Duration	DATE
Lecturer	Shyog campus, IIT Polytechnic , Nanded.	2Month	Aug-OCT 2015
Asst.Prof	Government collage of engineering, karad.	15 Month	Working Now
Visiting Faculty	Government collage of engineering, karad.	2 years	Working Now

## Education

Education	College/University	Year of Passing	%Of marks
X	SSC Board Latur, Maharashtra	2006	85.73
XII	HSC Board Latur, Maharashtra	2008	64.33
B.E(E&TC)	Dr. Babasaheb Ambedkar Marathwada University, Aurangabad.	2012	68.40
M-Tech(E.C.E)	Jawaharlal Nehru Technological University, Hyderabad.	2014	85.69

### Technical Expertise:

**Languages** : C, VHDL programming, PIC Programming.  
**software** : code block software, Indra Logic,FPGA,vivado,xilinx.  
**Operating Systems** : Windows (7, XP),  
**Libraries** : Matlab, Xilinx(VHDL DESIGN) and micro wind software.

## **Career Profile**

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### **Academic Projects :**

#### **1. Project Title: Centralizing Energy Meter**

**Programming Language** : Embedded C and Vb  
**Team size** : 3

**Description:** which use in daily energy billing without manual processor all processor doing with gsm modeam.

#### **Responsibilities:**

- Designing the system.
- Module Coding and testing.

#### **2. Project Title: Leakage minimization of 10 t full adder using deep sub micron technology.**

**Programming Language** : VLSI design  
**Team size** :1

**Description:** in this project I used 10 T full adder with using deep submicron 70nm technology for logic full adder design circuit this logic used for apply circuit processor through which reduces consuming power ,area for circuit design and speed increases for used in circuit sleep transistor.

#### **Responsibilities:**

- Designing the system.
- Module Coding and testing.

#### **Academic seminar :**

1. 4G technology.
2. A SARS method for reliable spectrum sensing in multiband communication systems.
3. Green heterogonous small cell networks: toward reducing the co2 emission of mobile communication industry using uplink power adaptation.

#### **Achivements**

- Pooja Tanurkar, Venkata chary, N.Bhojanna, G.Sujatha, "A 4 Bit full adder using 10 T with reduced Leakage Power and Ground Bounce", IJVDCS –Sept-2014.
- Kamble Supriya S, Narkhedkar Geetanjali S,Sutar Satish S, P.S.Tanurkar " Distribution Transformer Monitoring and Controlling Using GSM Module " , IJR - March-2017.
- Under My Guidance B.E project group working on "Distribution Transformer Monitoring and Controlling using GSM" .
- Wordsworth Club and Electro chaser Club Faculty Co-ordinator In GCE Karad.

- Designed a curriculum of **Network Analysis** course of S.Y.B.Tech E&TC.
- Designed a Curriculum Of **Computer Origination** course of T.Y.B.Tech E & TC.
- Designed a Curriculum of **Random Signal Processing** course of T.Y.B.Tech E & TC.
- Designed a Curriculum of **Video Engineering, satellite Communication and Broadband Communication** course of Final Year B.Tech E & TC.

### **Extra Activities**

- Committee Member of Social Gathering 2017 In GCE karad.
- Attended a workshop on Pedagogy to synergogy in May-June 16.
- Attended a workshop on PLC in Oct'16.
- Attended Microcontroller and Embedded workshop in Dec'15.
- Attended Advanced Pedagogy workshop in Oct'16.
- Attended a workshop on Basic Electro pneumatics in Dec' 16.
- Attended a Workshop on Variable Frequency Drive.
- Attended a 1 Week Workshop on "Python ,Machine Learning and Artificial Intelligence" on 11th Feb to 17th Feb 2017.
- Organized training program ( workshop) on "Real world interfacing to Microcontroller 8051 using assembly and Embedded C" for S.Y.B.Tech students.
- Conducted Industry Expert Lecture on "Audio Video Engineering" For Final Year Students.
- Organized Industry Visit to CSIR-National Institute Of Oceanography, Dona Paula,Goa Government organization for B.E EnTc student.
- Organized Industry Visit to laxmi Rickshaw Body Pvt.Ltd Aurangabad for S.Y. EnTC.
- Organized Industry Visit to Viral Electronics Pvt.Ltd Nasik for B.E. EnTC on 25th and 26th July 2017.
- Organized training program ( workshop) on "Vivado Software using Zybo and Zed broad VLSI Design" for T.Y.B.Tech students.
- **worked as External Senior Examiner of university Examinaion.**

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### **Personal Particulars**

- **Date of Birth:** July 5, 1991
- **Languages Known:** Hindi, English, Marathi and Telgu.
- **Nationality** - Indian

### **Declaration**

I hereby affirm that the information furnished above is true to the best of my knowledge.

**POOJA TANURKAR**

