

Utkarsha Harishchandra Gharge
Electronics & Telecommunication Engineer
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CAREER OBJECTIVE

To work in a VLSI and Embedded firm with a professional work driven environment where I can utilize and apply my knowledge, skills which would enable me to grow while fulfilling organizational goals.

SUMMARY

- **1.3+year** of experience in Embedded C and VLSI.
- Good experience in VHDL and verilog programming.
- Good experience in C and embedded C programming.
- Project done using ESP32 and Arduino Uno.
- Good understanding of IoT.
- Good understanding of the ASIC and FPGA design flow.
- Experience in using industry standard EDA tools for the front end design and verification.
- Good practice of Xilinx and Vivado software.
- Schematic designing using Microwind and xilinx.
- Knowledge of MATLAB, tina.
- Strengths are in team player, problem solving capabilities, defining work priorities

INDUSTRIAL EXPERIENCE (1.3+ Year)

- 11 months (Jan-2021 to Present) experience with ESP32 and Arduino as a Hardware Developer at **GIZMO Pvt. Ltd, Pune**
- 3 months (Oct-2020 to Dec-2020) experience with dspic33f32 and tms320f280x as a Embedded Project Developer at **SARPCON Pvt. Ltd, Chennai**

PROJECT DETAILS

- **Water level Controller using LoRaWAN Technology-** at GIZMO, Pune.

Duration:-Jan 2021-May 2021

Description:-In this project, Our team has designed water level controller using LoRa technology. To detect level of water, we have designed conduction probe circuit using comparator. This circuit is less costly than sensors available in market. Lora is used to send data from upper level tank to lower level tank.MQTT is used for android application.

Language used : Embedded C and C.

Tools used : VS Code

Implementation : ESP32,Arduino.

- **Thermal management of FPGA based single core processor** at CoreEL Technologies, Bangalore

Duration:-July 2019-Oct 2019

Description:-This project is useful to sense the internal temperature of FPGA i.e. extraction of heat and controlling this temperature with the help of peltier. The temperature sensor is configured with microblaze which generates some internal heat which is sensed by the sensor and displayed on the seven segment display.

HDL used : VHDL

Tools used : Xilinx 14.4, XPS, SDK

Implementation : on Spartan 3E

SKILLS

- **OS:** Windows, Linux(Ubuntu)
- **Programming:** C, C++, Embedded C, Verilog, VHDL, System verilog
- **Compilers:** Keil V4, Visual Studio ,Code blocks
- **Softwares:** Code composer studio, Mplab.
- **Microcontrollers:**ESP32, 8085, 8051, ARDUINO,DsPIC33FJ32, TMS320
- **Communication Protocol:** I2C, SPI, UART
- **VLSI softwares:** Xilinx, Vivado, Microwind, Modelsim Altera

ACHIEVEMENTS

- Completed short term course on "Designing with FPGA" at Sandeepani school of embedded system design
- Achieved 2nd prize in robotics competition held by ARK technologies.
- Completed short term course on "Physical Design and verification RTL2GDSII" at Sandeepani school of embedded system design.

EDUCATION

Degree	Percentage	Year of Passing	University
M.Tech (VLSI and Embedded)	87.5%	2019-2021	Pune University
B.Tech (ENTC)	71%	2014-2018	Shivaji University
H.S.C	73%	2013-2014	Kolhapur Board
S.S.C	90%	2011-2012	Kolhapur Board

Personal Information:-

Date of Birth	:	23 th April 1997
Nationality	:	Indian
Gender	:	Female
Marital Status	:	Single
Languages Known	:	English, Hindi and Marathi.
Special Interests	:	Playing Badminton
Mobile	:	9970612677
Personal E-mail ID	:	ghargeutkarsha@gmail.com
Permanent Address	:	Gajvadan Garden ,Satara Tal:-Satara, Dist.:-Satara, Pin Code:-415002

DECLARATION: - I hereby declare that the information provided above is true to the best of my knowledge and belief. If have given opportunity I can assure to smart work to the satisfactions of my superior.

PLACE: -

(Utkarsha Gcharge)

